

Lecture 37 : Uniformity of Circuits

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LECTURE PLAN: In this lecture, we complete the proof of $P \setminus \text{poly} = \text{PSIZE}$. We talk about a special class of polynomial sized circuit families called P-Uniform circuits and characterize the languages associated with these circuit families. We finally prove that the class of such languages is actually P.

Proof of $\text{PSIZE} = P \setminus \text{poly}$

Recall the definition of PSIZE and $P \setminus \text{poly}$ from the previous lectures.

Definition 1.

$$\text{PSIZE} = \bigcup_{k \geq 0} \text{SIZE}(n^k)$$

It is the class of all those languages that can be computed by a family of polynomial sized circuits.

Definition 2. A language L is in $P \setminus \text{poly}$ if there exists a polynomial sized advice string $h : \mathbb{N} \rightarrow \{0, 1\}^*$, there exists a language $B \in P$ such that,

$$x \in L \iff (x, h(|x|)) \in B$$

In the last lecture we saw the forward containment, that is, $\text{PSIZE} \subseteq P \setminus \text{poly}$. The idea was to provide as an advice string, for a given input x , the circuit $C_{|x|}$. The resultant machine takes the input x and the advice $C_{|x|}$ and checks whether the circuit evaluates to true on input x . Observe that this computation is essentially a *Circuit Value Problem* instance which is in P. We now prove the reverse containment, that is $P \setminus \text{poly} \subseteq \text{PSIZE}$.

Given a language $L \in P \setminus \text{poly}$, and an input string x , the computation is deterministic when we have the associated advice $h(|x|)$. Also, the no. of configurations in the computation is polynomial in size since the length of h is polynomial in $|x|$. Each bit of a configuration in the deterministic computation is uniquely determined by exactly 3 bits of the previous configuration. Hence, for each bit i of a configuration j , we find a circuit that takes the three bits determining bit i from the configuration $j - 1$. Of course, the size of this circuit is constant. We repeat this for each bit in each configuration of the run. Observe that this circuit depends only on the length of x and not the input x itself. We are essentially

hardwiring $h(|x|)$ into this circuit to obtain $C_{|x|}$. Hence, we get a family of polynomially sized circuits computing L .

One subtlety that needs to be taken care of is the input to the circuit. The input will be the start configuration of the machine B along with the advice h . Depending upon the advice for a particular string length, a different circuit is formed. This leads to what is referred to as *non-uniformity* in circuits. Circuits of a family deciding the same language may differ in size and depth depending on the advice. Since, the family is infinite, it is not possible to describe these non-uniform circuits in polynomial time or space.

1 Uniform Circuits

To address this issue, let us find the source of the non-uniformity in our previous construction. Given an input x , we do the following:

1. Obtain the description of $C_{|x|}$ using the initial configuration, the input length *and the advice* $h(|x|)$.
2. Run the circuit on x .

Non-uniformity in the circuits is induced due to step 1, since the advice varies with the string length. We look at a specific class of circuits where this is eliminated.

1.1 P-Uniform Circuits

Consider the class of languages that can be decided in polynomial time. We know that $P \subseteq P \setminus \text{poly}$. The natural question is to ask for an advice string an input x for a language $L \in P$. by definition of $P \setminus \text{poly}$, observe that once an advice is given, the remaining computation needs to be done in polynomial time. Since, L itself can be decided in polynomial time, we do not require an advice. In other words, any trivial advice works for deciding all inputs x . Hence, in the above step 1, the circuit C_n can be described depending only on the input length n . Such a family of circuits is said to be *P-Uniform*.

Definition 3. A circuit family $C_n, n \geq 0$ is said to be *P-Uniform* if there exists a polynomial time deterministic Turing machine which on input 1^n outputs the description of C_n .

Claim 4. *The languages computed by P-Uniform circuit families can be decided in P.*

Proof. Let us denote the class of languages decided by P -Uniform circuits by UPSIZE. By our earlier discussion, we know that any language in P (without advice) can be computed by a P -Uniform circuit family. Hence $P \subseteq \text{UPSIZ}$ E. We now have to prove that UPSIZ E $\subseteq P$. Consider a language $L \in \text{UPSIZ}$ E. Let M be the polynomial time TM that outputs the description of C_n on input n . We construct a new machine N to decide L as follows:
 N on input x ,

1. Run M on $|x|$ to obtain $C_{|x|}$.
2. Check if $(C_{|x|}, x) \in \text{CVP}$. If yes, accept x , else reject.

Machine M runs in polynomial time in step 1 and the checking in step 2 takes polynomial time since $\text{CVP} \in P$ (Circuit Value Problem). Hence, $L(N) \in P$. Also, since N accepts only those strings for which the corresponding circuit of the circuit family of L outputs 1. Hence $L(N) = L$. Hence UPSIZ E $\subseteq P$. \square

Now that we know that UPSIZ E = P , the natural question to ask is $\text{NP} = \text{UPSIZ}$ E. That is, if there exists a P -Uniform circuit deciding SAT. Aiming to prove this is intuitively more tough than proving if there is any polynomial sized circuit at all deciding SAT. This goes well with our turing machine world where proving that $\text{SAT} \in P \setminus \text{poly}$ is intuitively an easier task than proving if $\text{SAT} \in P$, since we know that $P \subseteq P \setminus \text{poly}$.