RAHUL SHRIVASTAVA
MS Scholar , 2nd year,
PACE Lab, CSE Dept,
IIT Madras
Mobile no: +91-8144118969
rahuls@cse.iitm.ac.in

PROFILE

A Research Scholar specializing in the design and coding of compilers for generating energy efficient high performance task parallel programs . Strengths include

- Compilers
- Operating Systems
- Data structures

RESEARCH ACTIVITIES

- Working on X10 compiler which will take as input the parallel X10 program and generate the energy efficient parallel code with minimal loss in performance using the "Dynamic voltage and frequency scaling" constructs

PROFESSIONAL EXPERIENCE (3+ years)

Teaching Assistant for

- Compiler Design for 3rd year B.Tech. students from July 2014 – Nov 2014
- Basic C programming for 1st year students from Jan 2015 - present

Tata Consultancy Services Ltd, Mumbai
System Engineer **Sep 2010 – Dec 2013**

Project Description: VSE is a Versatile Service Engine which is ATCA compliant 16 blade shelf to provide telecom signalling support spread across Austria

RESPONSIBILITIES

- Integration of open source GlusterFS with the VSE
- Took initiative in Converting the shell script to install RPMs in the system to C code so that it speeds up the execution and enhance the performance of VSE
- Responsible for Design and development of enhanced features as per user requirements
- Worked in upgrade feature to introduce new blade and integrate the blades in the VSE
- Imparted knowledge transfer session to new team members understanding the architecture of the VSE

TECHNICAL SKILLS

Languages	X10, C, C++, shell script
Middleware	openSAF
OS	Red Hat Linux, Windriver NCGL linux, Windows
Tools	Guru, Clearcase, IDCE, Clarify, svn, usim, gem5, snipersim
FileSystem	GlusterFS

ACADEMIC PROJECTS

- **Project made in under graduation**

  LAN search Engine: It is a search engine to search files in the LAN which can search the path of files which are stored in different hosts of a particular network

  Technology: Java

- **Project made during post graduation**
  - Minijava to Microjava translator
  - Microjava interpretor
  - Implemented a paper "Scalable parallel minimum spanning forest computation" presented in PPOPP'12
  - Implemented Parallel grep

EDUCATION

- Pursuing MS in Computer Science from IIT Madras since JAN 2014(**CGPA 8.4**)
- B.E. Computer Science and Engineering (2006-2010) from Jabalpur Engg. College, Jabalpur with an aggregate of **72.93%**
- 12th (MP board) from Nachiketa Higher Secondary School, Jabalpur, with **81.3%**
- 10th (MP board) from Nachiketa Higher Secondary School, Jabalpur, with **85%**