OPERATING SYSTEMS CS3500

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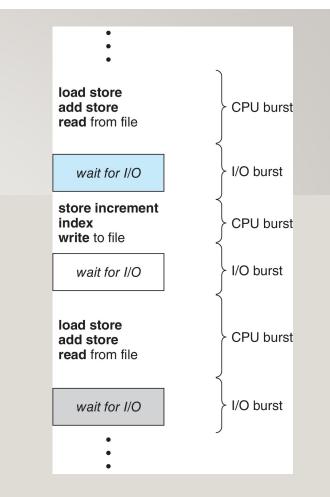
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CPU SCHEDULING

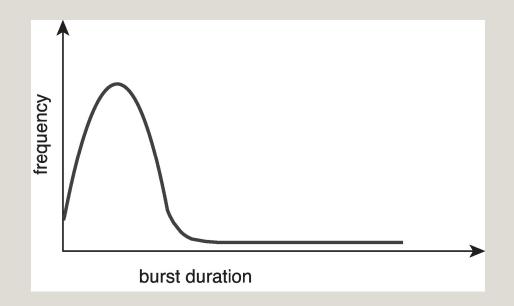
Basic Concepts

- Maximum CPU utilization obtained with multiprogramming
- CPU-I/O Burst Cycle Process execution consists of a cycle of CPU execution and I/O wait
- CPU burst followed by I/O burst
- CPU burst distribution is of main concern



Histogram of CPU-burst Times

- Large number of short bursts
- Small number of longer bursts



CPU Scheduler

The CPU scheduler selects from among the processes in ready queue, and allocates a CPU core to one of them

- Queue may be ordered in various ways
- > CPU scheduling decisions may take place when a process:
 - I. Switches from running to waiting state
 - 2. Switches from running to ready state
 - 3. Switches from waiting to ready
 - > Terminates
- For situations I and 4, there is no choice in terms of scheduling. A new process (if one exists in the ready queue) must be selected for execution.
- > For situations 2 and 3, however, there is a choice



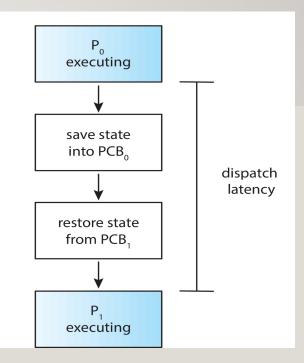
Preemptive and Nonpreemptive Scheduling

- When scheduling takes place only under circumstances I and 4, the scheduling scheme is nonpreemptive.
- > Otherwise, it is **preemptive**.
- Under Nonpreemptive scheduling, once the CPU has been allocated to a process, the process keeps the CPU until it releases it either by terminating or by switching to the waiting state.
- Virtually all modern operating systems including Windows, MacOS, Linux, and UNIX use preemptive scheduling algorithms.
- Preemptive scheduling can result in race conditions when data are shared among several processes.
- Consider the case of two processes that share data. While one process is updating the data, it is preempted so that the second process can run. The second process then tries to read the data, which are in an inconsistent state.



Dispatcher

- Dispatcher module gives control of the CPU to the process selected by the CPU scheduler; this involves:
 - Switching context
 - Switching to user mode
 - Jumping to the proper location in the user program to restart that program
- Dispatch latency time it takes for the dispatcher to stop one process and start another running



Scheduling Criteria

- CPU utilization keep the CPU as busy as possible
- Throughput # of processes that complete their execution per time unit
- > **Turnaround time** amount of time to execute a particular process
- Waiting time amount of time a process has been waiting in the ready queue
- Response time amount of time it takes from when a request was submitted until the first response is produced

Scheduling Algorithm Optimization Criteria

What should be optimization criteria???

- Max CPU utilization
- > Max throughput
- Min turnaround time
- > Min waiting time
- > Min response time

First- Come, First-Served (FCFS) Scheduling

Process	<u>Burst Time</u>
P,	24
P_2	3
P ₃	3

Suppose that the processes arrive in the order: P_1 , P_2 , P_3 The Gantt Chart for the schedule is:



Waiting time for P₁ = 0; P₂ = 24; P₃ = 27
 Average waiting time: (0 + 24 + 27)/3 = 17

FCFS Scheduling (Cont.)

Suppose that the processes arrive in the order:

$$P_2$$
, P_3 , P_1

> The Gantt chart for the schedule is:

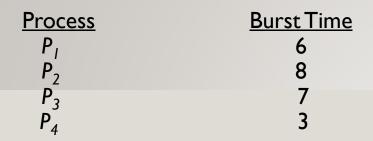


- > Waiting time for $P_1 = 6$; $P_2 = 0$; $P_3 = 3$
- > Average waiting time: (6 + 0 + 3)/3 = 3
- Much better than previous case
- Convoy effect short process behind long process
 - Consider one CPU-bound and many I/O-bound processes

Shortest-Job-First (SJF) Scheduling

- Associate with each process the length of its next CPU burst
 - Use these lengths to schedule the process with the shortest time
- SJF is optimal gives minimum average waiting time for a given set of processes
- Preemptive version called shortest-remaining-time-first
- How do we determine the length of the next CPU burst?
 - Could ask the user
 - Estimate

Example of SJF



SJF scheduling chart

	P_4	P_1	P ₃	P ₂
0	3	9) 10	6 24

Average waiting time = (3 + 16 + 9 + 0) / 4 = 7

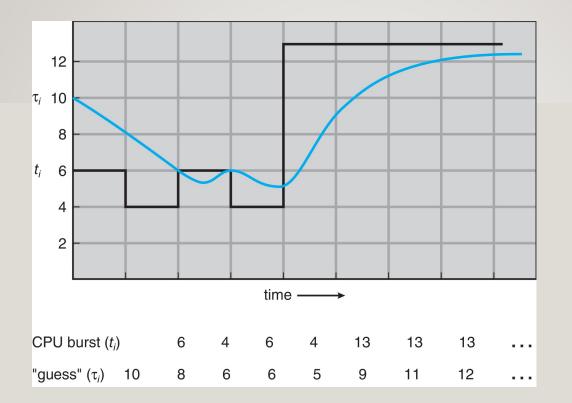
Determining Length of Next CPU Burst

- Can only estimate the length should be similar to the previous one
 Then pick process with shortest predicted next CPU burst
- Can be done by using the length of previous CPU bursts, using exponential averaging
 - 1. t_n = actual length of n^{th} CPU burst
 - 2. T_{n+1} = predicted value for the next CPU burst
 - $\textbf{3.} \hspace{0.2cm} \boldsymbol{\mathcal{\alpha}}, \hspace{0.1cm} \textbf{0} \leq \hspace{0.1cm} \boldsymbol{\mathcal{\alpha}} \leq \hspace{0.1cm} \textbf{1}$
 - 4. Define:

 $\tau_{n+1} = \alpha t_n + (1-\alpha)\tau_n.$

> Commonly, α set to $\frac{1}{2}$

Prediction of the Length of the Next CPU Burst



Examples of Exponential Averaging

 $\begin{array}{l} \geqslant \alpha = 0 \\ \geqslant \tau_{n+1} = \tau_n \\ \geqslant \text{ Recent history does not count} \\ \end{array} \\ \begin{array}{l} \geqslant \alpha = 1 \\ \geqslant \tau_{n+1} = \alpha t_n \\ \geqslant \text{ Only the actual last CPU burst counts} \\ \end{array} \\ \begin{array}{l} \geqslant \text{ If we expand the formula, we get:} \\ \qquad \geqslant \tau_{n+1} = \alpha t_n + (1 - \alpha)\alpha t_{n-1} + \dots \\ \qquad \Rightarrow \quad + (1 - \alpha)^j \alpha t_{n-j} + \dots \\ \qquad \qquad \Rightarrow \quad + (1 - \alpha)^{n+1} \tau_0 \end{array}$

> Since both α and $(1 - \alpha)$ are less than or equal to 1, each successor predecessor term has less weight than its predecessor

Shortest Remaining Time First Scheduling

- Preemptive version of SJN
- Whenever a new process arrives in the ready queue, the decision on which process to schedule next is redone using the SJN algorithm.
- Is SRT more "optimal" than SJN in terms of the minimum average waiting time for a given set of processes?

Example of Shortest-remaining-time-first

> Now we add the concepts of varying arrival times and preemption to the analysis

Process	Burst Time	<u>Arrival Time</u>
P	8	0
P_2	4	I
P_3	9	2
P ₄	5	3
-		

Preemptive SJF Gantt Chart

	P_1	<i>P</i> ₂	P_4	P_1	P ₃
() (1 5	1	0 1	7 26
Average	ge v	vaiting tir	ne = [(10-1)+	·(-)+(7-2)+(5-3)]/4 = 26/4 = 6.5

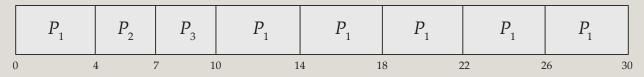
Round Robin (RR)

- Each process gets a small unit of CPU time (time quantum q), usually 10-100 milliseconds. After this time has elapsed, the process is preempted and added to the end of the ready queue.
- If there are n processes in the ready queue and the time quantum is q, then each process gets 1/n of the CPU time in chunks of at most q time units at once. No process waits more than (n-1)q time units.
- Timer interrupts every quantum to schedule next process
- > Performance
 - \succ q large ⇒ FIFO (FCFS)
 - \succ q small ⇒ RR
- Note that q must be large with respect to context switch, otherwise overhead is too high

Example of RR with Time Quantum = 4

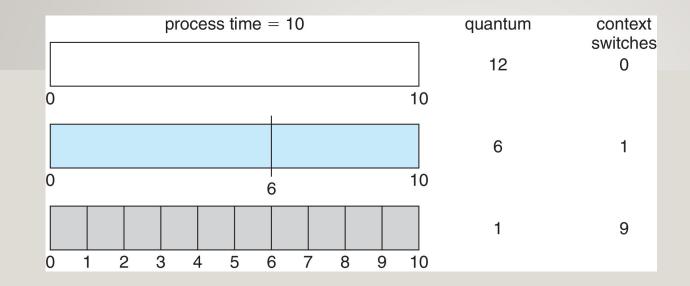
\triangleright	<u>Process</u>	<u>Burst Time</u>
	P	24
	P_2'	3
	P ₃	3

The Gantt chart is:

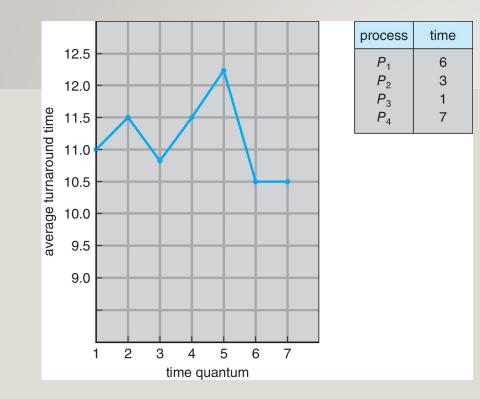


- > Typically, higher average turnaround than SJF, but better **response**
- q should be large compared to context switch time
 - \succ q usually 10 milliseconds to 100 milliseconds,
 - Context switch < 10 microseconds</p>

Time Quantum and Context Switch Time



Turnaround Time Varies With The Time Quantum



80% of CPU burst should be shorter than qs

Priority Scheduling

- > A priority number (integer) is associated with each process
- The CPU is allocated to the process with the highest priority (smallest integer = highest priority)
 - Preemptive
 - > Nonpreemptive
- SJF is priority scheduling where priority is the inverse of predicted next CPU burst time
- Problem = Starvation low priority processes may never execute
- > Solution = Aging as time progresses increase the priority of the process

Example of Priority Scheduling

<u>Process</u>	<u>Burst Time</u>	<u>Priority</u>
P	10	3
P ₂	I	I
P ₃	2	4
P_4	I	5
P_5	5	2

Priority scheduling Gantt Chart

P2	P ₅	P ₁	P_{3}	P ₄
0	1 (6 1	6	18 19

Average waiting time = 8.2

Priority Scheduling w/ Round-Robin

- Run the process with the highest priority. Processes with the same priority run round-robin
- > Example:

<u>Process</u>	Burst Time	<u>Priority</u>
P ₁	4	3
P ₂	5	2
P ₃	8	2
P_4	7	I
P_5	3	3

Gantt Chart with time quantum = 2

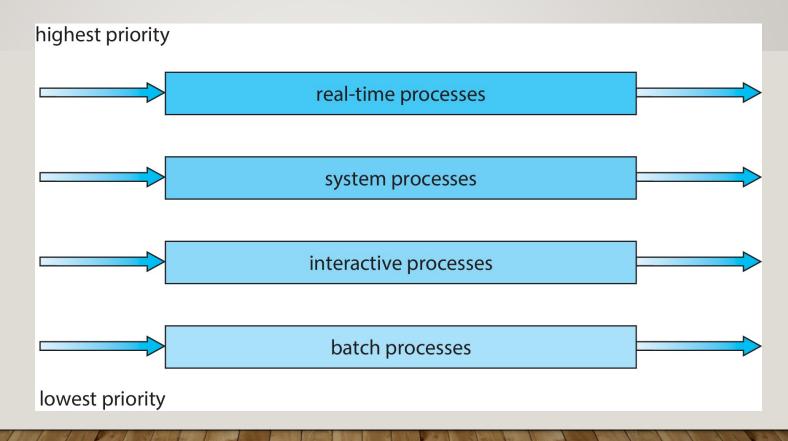
	Ρ ₄	P ₂	Р ₃	P ₂	P ₃	P ₂	P ₃	P ₁	P ₅	P ₁	P ₅
0	7	7 9) 11	I 1	3 1.	5 16	5 2	0 22	2 2	4 2	6 27

Multilevel Queue

The ready queue consists of multiple queues	priority = 0	T _o	T ₁	T ₂	T ₃	T ₄
Multilevel queue scheduler defined by the following						
 parameters: Number of queues Scheduling algorithms for each queue Method used to determine which queue a process 	priority = 1	T ₅	T ₆	T ₇		
 will enter when that process needs service Scheduling among the queues 	priority = 2	T ₈	T ₉	T ₁₀	T ₁₁	
With priority scheduling, have separate queues for eac priority. Then use Round-robin	:h	•				
Schedule the processes in the highest-priority queue!						
	priority = n	T _x	T _y	T _z		

Multilevel Queue

Prioritization based upon process type; eg Foreground vs background



The foreground queue might be scheduled by an RR algorithm, for example, while the background queue is scheduled by an FCFS algorithm.

There must be scheduling among the queues, which is commonly implemented as fixedpriority preemptive scheduling. For example, the real-time queue may have absolute priority over the interactive queue.

An example of a multilevel queue scheduling algorithm with four queues, listed below in order of priority:

- 1. Real-time processes
- 2. System processes
- 3. Interactive processes
- 4. Batch processes

No process in the batch queue, for example, could run unless the queues for real-time processes, system processes, and interactive processes were all empty. If an interactive process entered the ready queue while a batch process was running, the batch process would be preempted.

Time-slice among the queues: each queue gets a certain portion of the CPU time, which it can then schedule among its various processes. For instance, in the foreground–background queue example, the foreground queue can be given 80 percent of the CPU time for RR scheduling, among its processes, while the background queue receives 20 percent of the CPU to give to its processes on an FCFS basis.

Multilevel Feedback Queue

- A process can move between the various queues. The idea is to separate processes according to the characteristics of their bursts. If a process uses too much CPU time, it will be moved to a lower-priority queue
- Multilevel-feedback-queue scheduler defined by the following parameters:
 Number of queues
 - > Scheduling algorithms for each queue
 - > Method used to determine when to upgrade a process
 - > Method used to determine when to demote a process
 - Method used to determine which queue a process will enter when that process needs service
- Aging can be implemented using multilevel feedback Q. To prevent starvation, a process that waits too long in a lower-priority Q may gradually be moved to a higher-priority Q.

Example of Multilevel Feedback Queue

- Three queues:
 - $> Q_0 RR$ with time quantum 8 milliseconds
 - \succ Q_1 RR time quantum 16 milliseconds
 - $> Q_2 FCFS$
- Scheduling
 - > A new process enters queue Q_0 which is served in RR
 - When it gains CPU, the process receives 8 milliseconds
 - If it does not finish in 8 milliseconds, the process is moved to queue Q₁
 - At Q₁ job is again served in RR and receives 16 additional milliseconds
 - If it still does not complete, it is preempted and moved to queue Q₂

