

Cs2310:Digital logic design Lab

Assignment 10

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1 Problem Statement

Write Verilog Code to implement the function $f(x_1, \dots, x_4) = \sum m(0, 1, 2, 4, 5, 7, 8, 9, 11, 12, 14, 15)$ using the gate level primitives. Ensure that the resulting circuit is as simple as possible